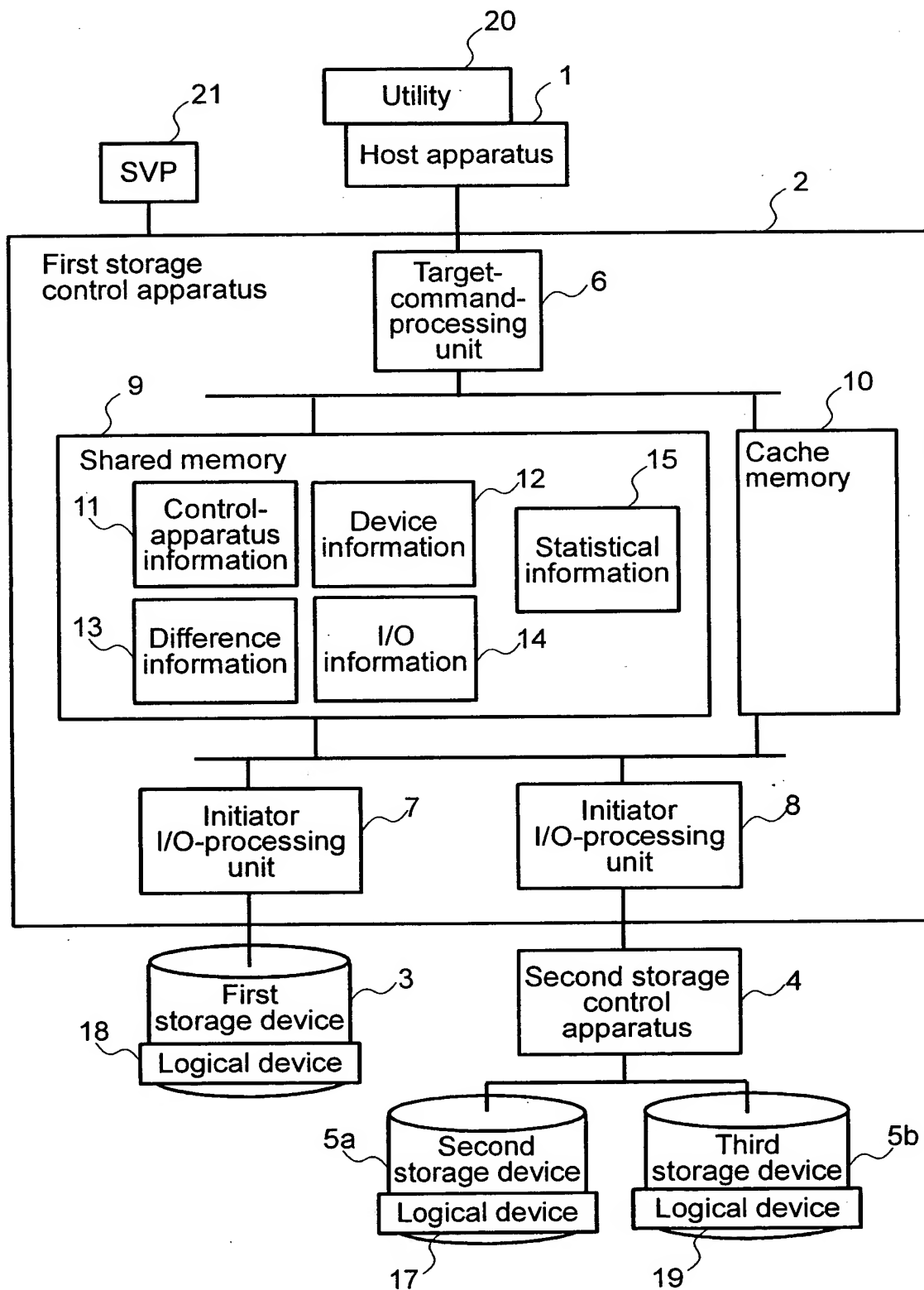


FIG. 1



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FIG.2

Control-apparatus number	Case location	Initiator control unit number	Node Name	Address
0	Self	0	HTC	0000E8
1	Other	1	SBC	0100E4
2	Not applicable	Not applicable	Not applicable	Not applicable
.
.
.

FIG.3

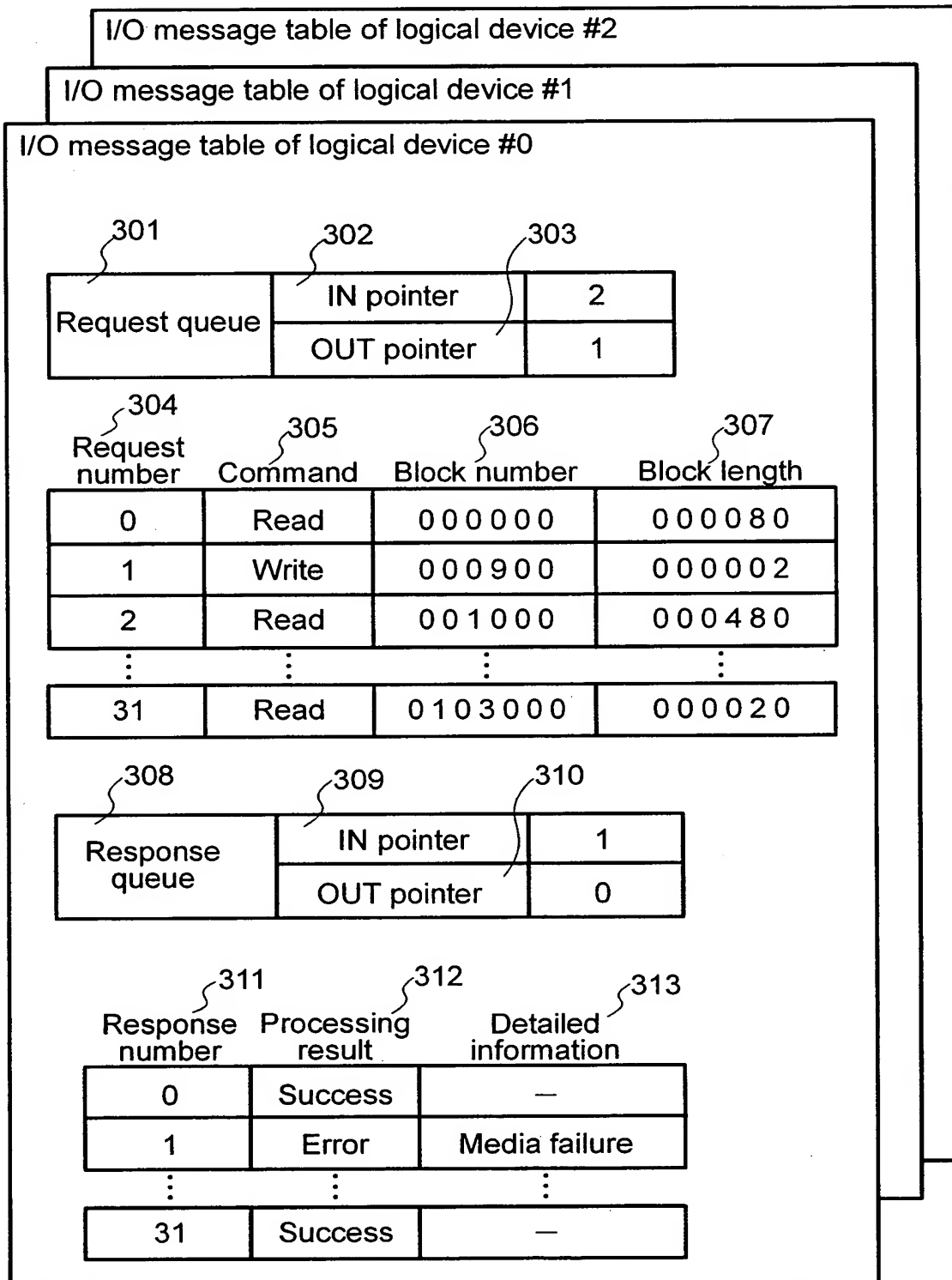
Logical-device number	Control-apparatus number	Storage-device number	Pair state	Pair number	Attribute	Pair logical device number	Copy pointer
0	0	0	Split	0	Primary	1	Not applicable
1	1	0	Split	0	Auxiliary	0	Not applicable
2	1	1	Simplex	Not applicable	Primary	Not applicable	Not applicable
3	Not applicable	Not applicable	Simplex	Not applicable	Primary	Not applicable	Not applicable
.
.
.

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FIG.4

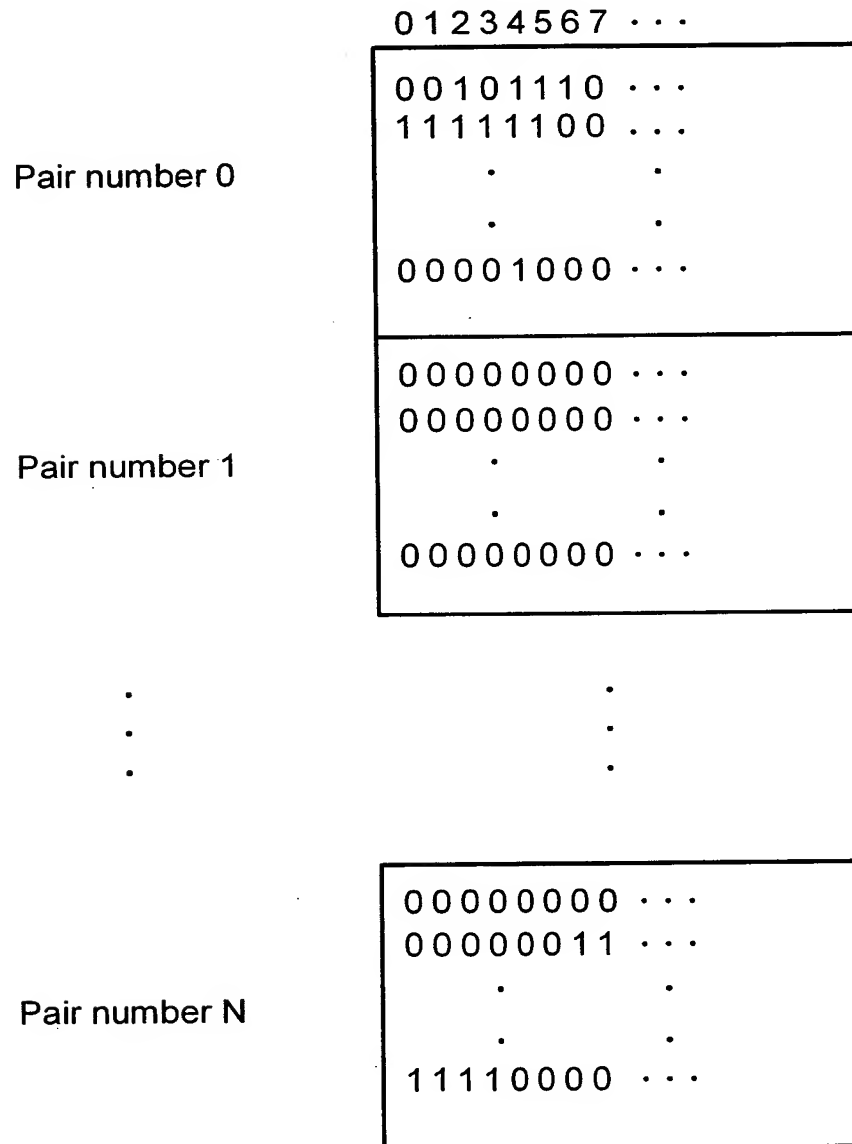
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FIG. 5



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FIG.6

401 Logical- device number	402 Access-I/O counter	403 Read-I/O counter	404 Write-I/O counter
0	0020	0010	0010
1	0010	0006	0004
2	0100	0100	0000
3	0000	0000	0000
⋮	⋮	⋮	⋮

FIG.7

Inputs:
Logical-device number

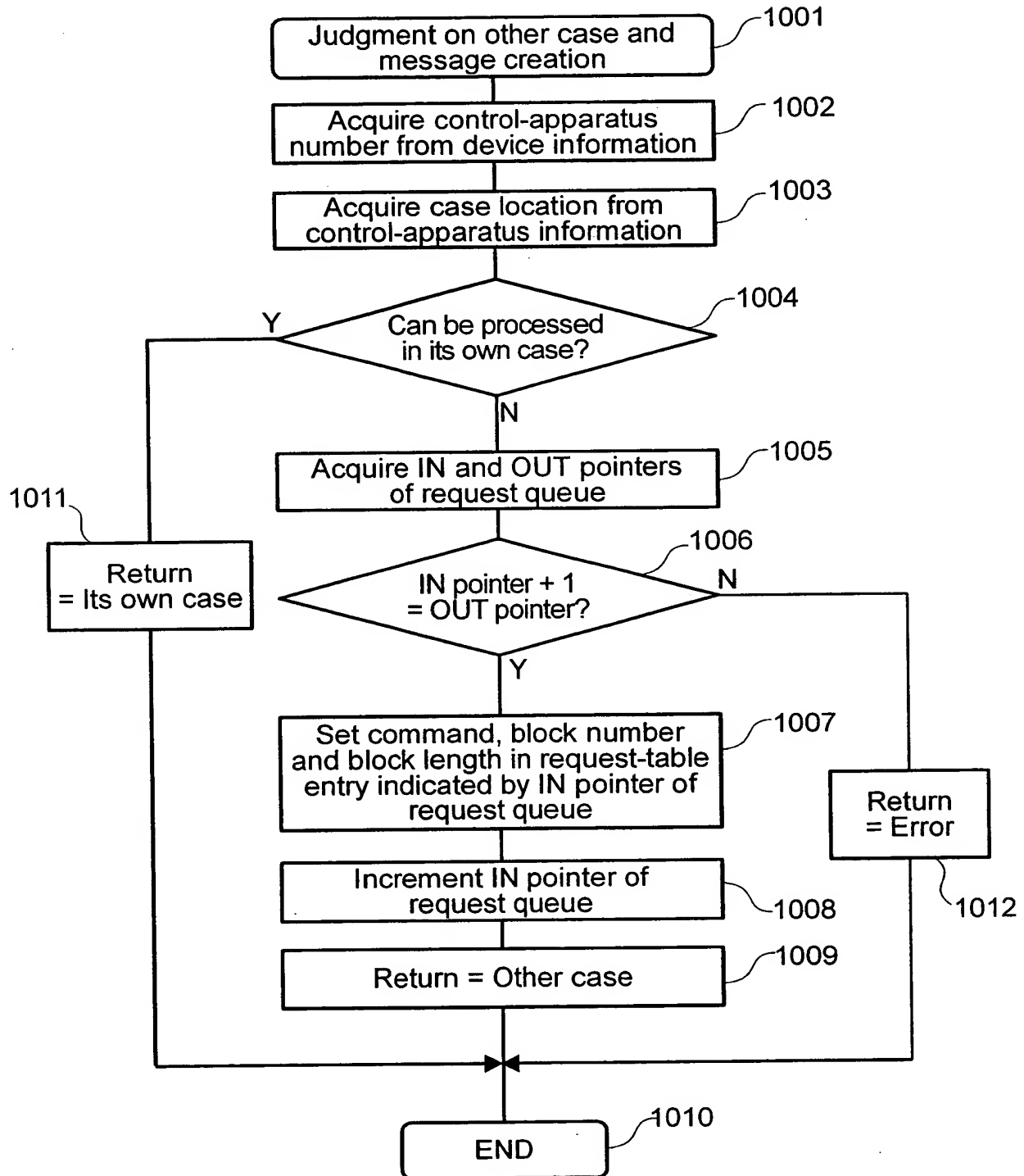


FIG. 8

Inputs:
Logical-device number

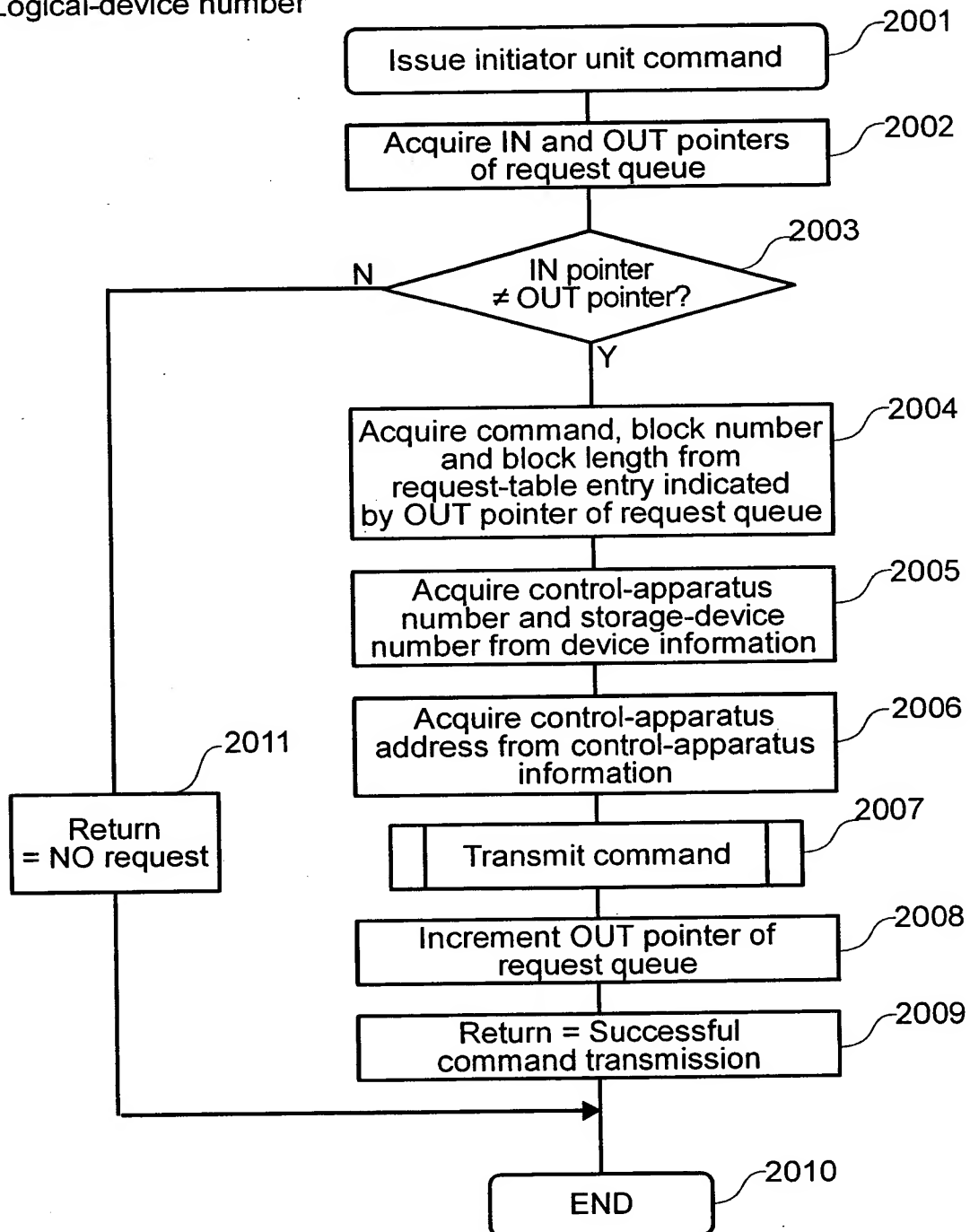
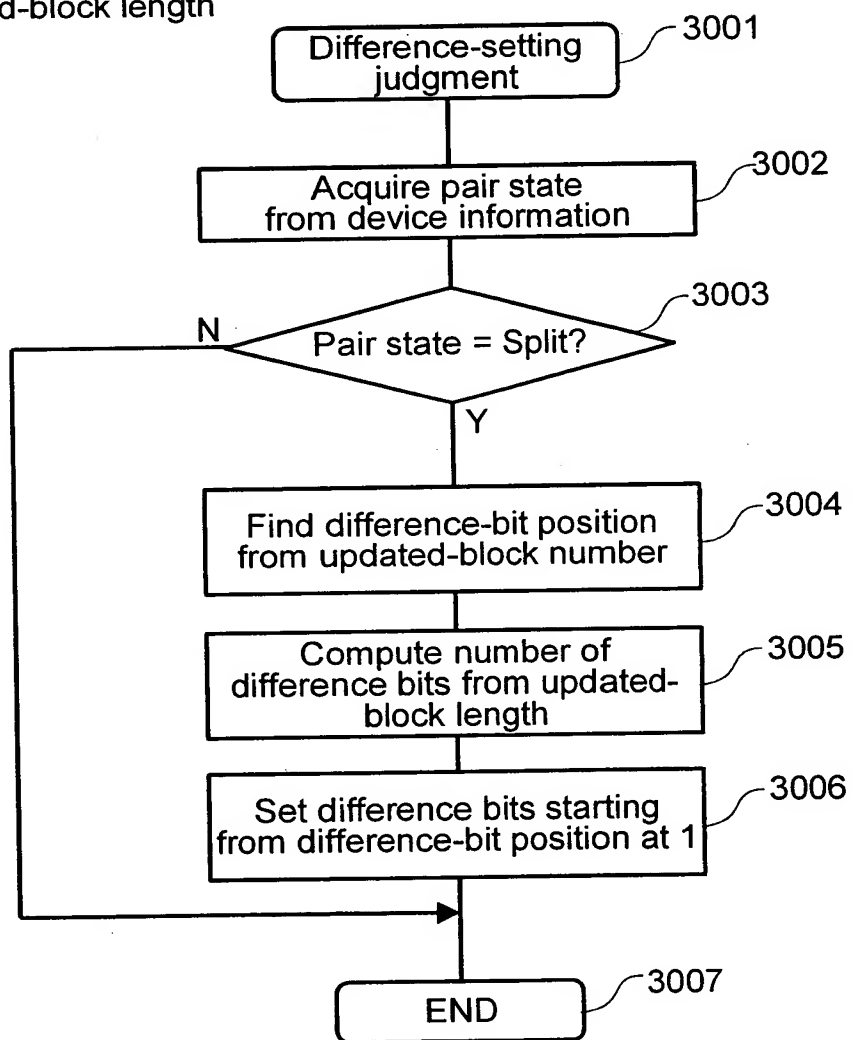


FIG.9

Inputs:
Logical-device number,
updated-block number
and updated-block length



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FIG. 10

Inputs:
Logical-device number

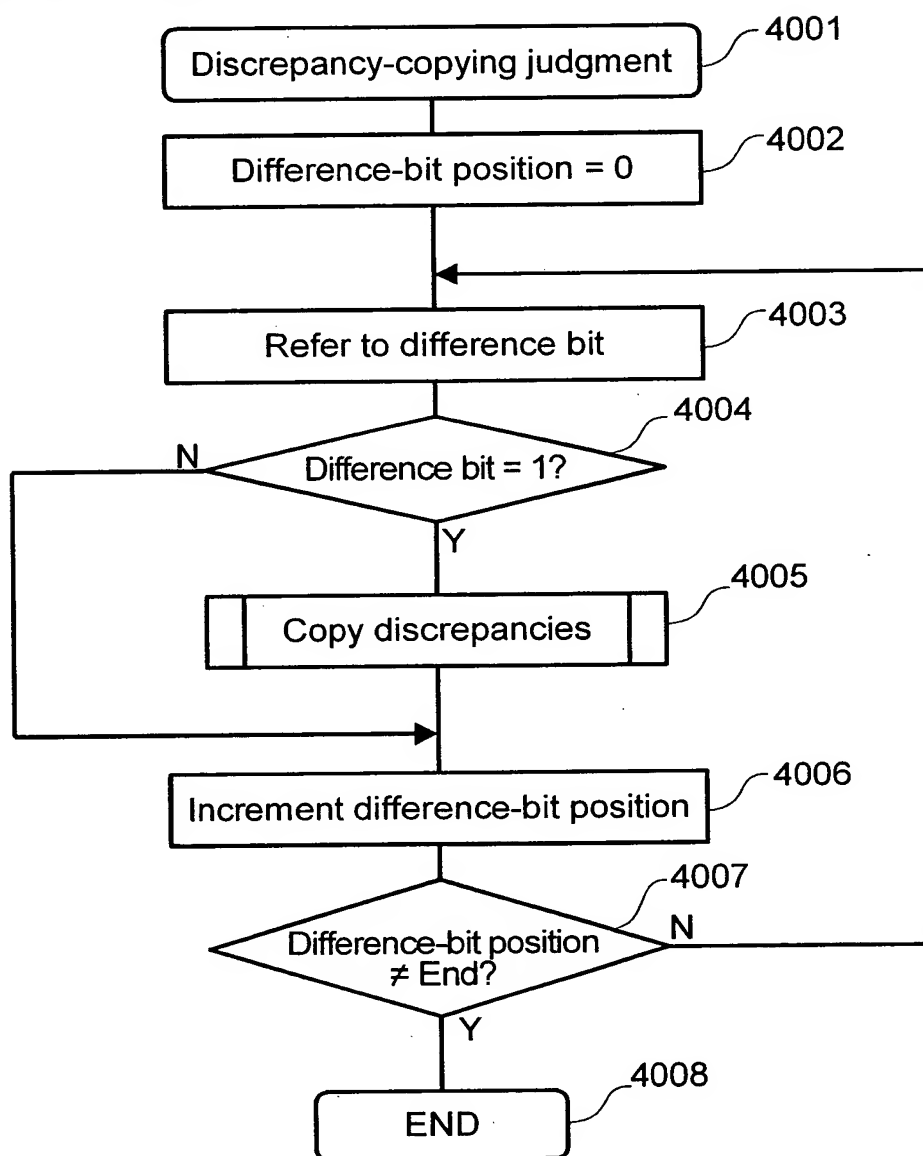


FIG.11

Inputs:
Logical-device number,
read-block number and
read-block count

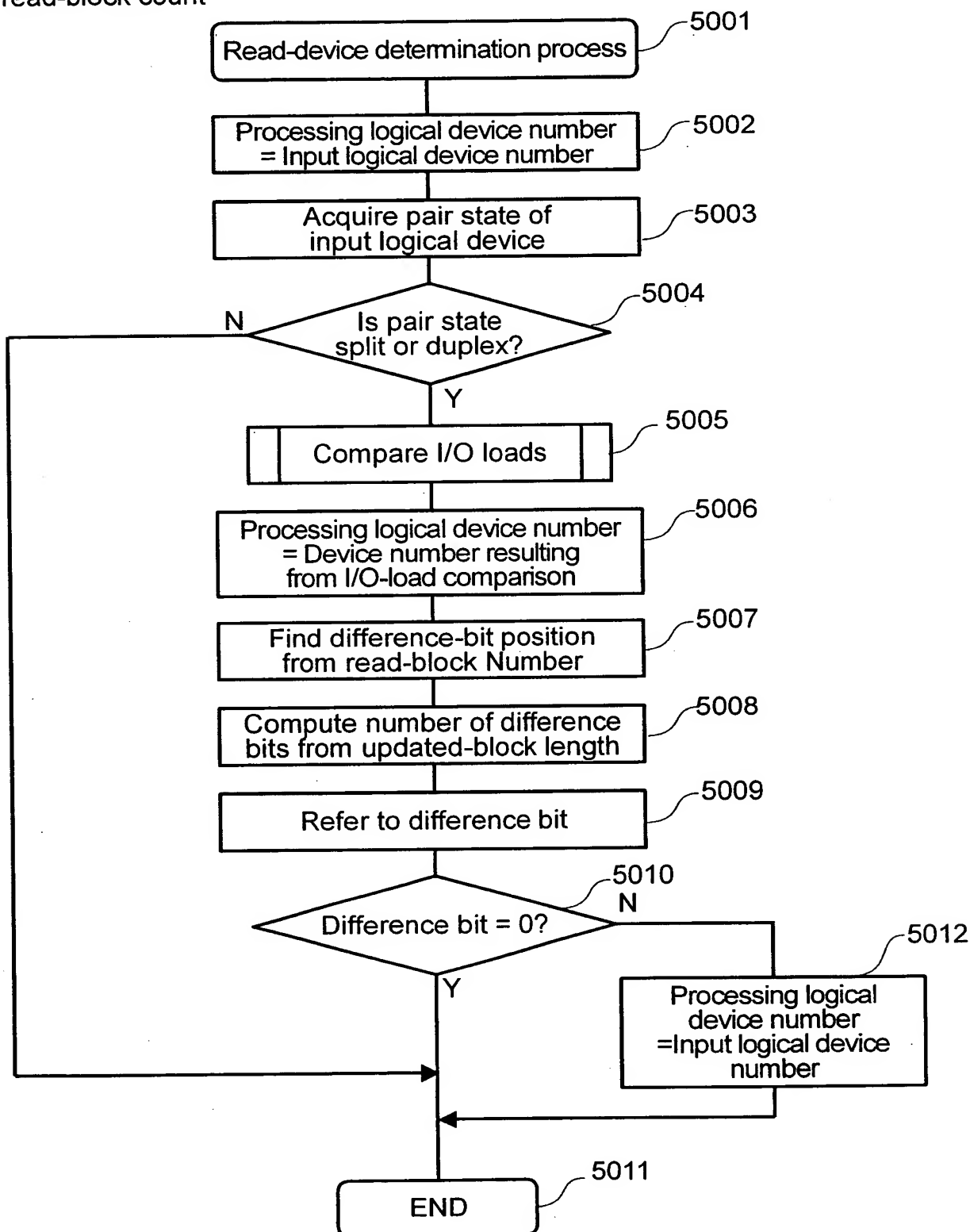


FIG.12

Inputs:
Logical-device number

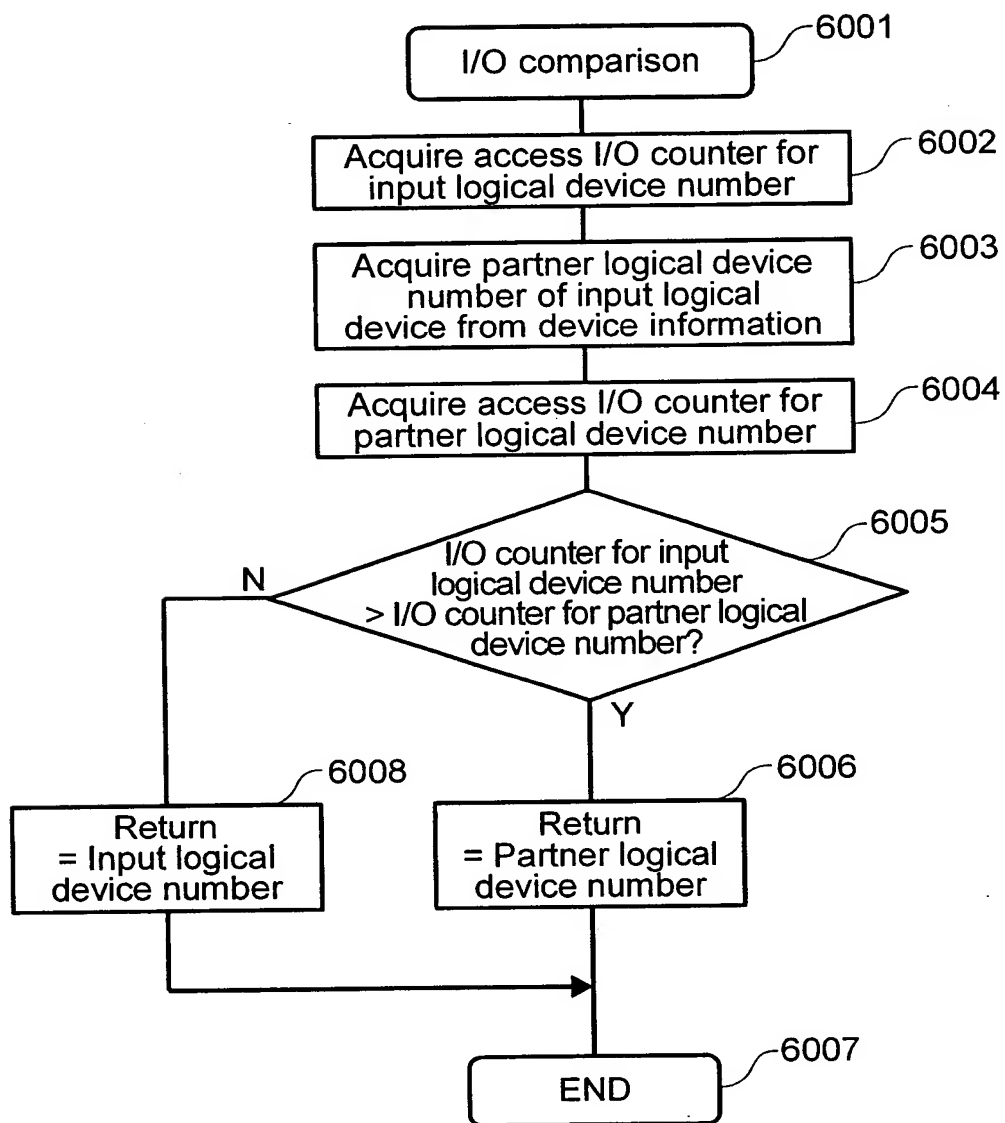
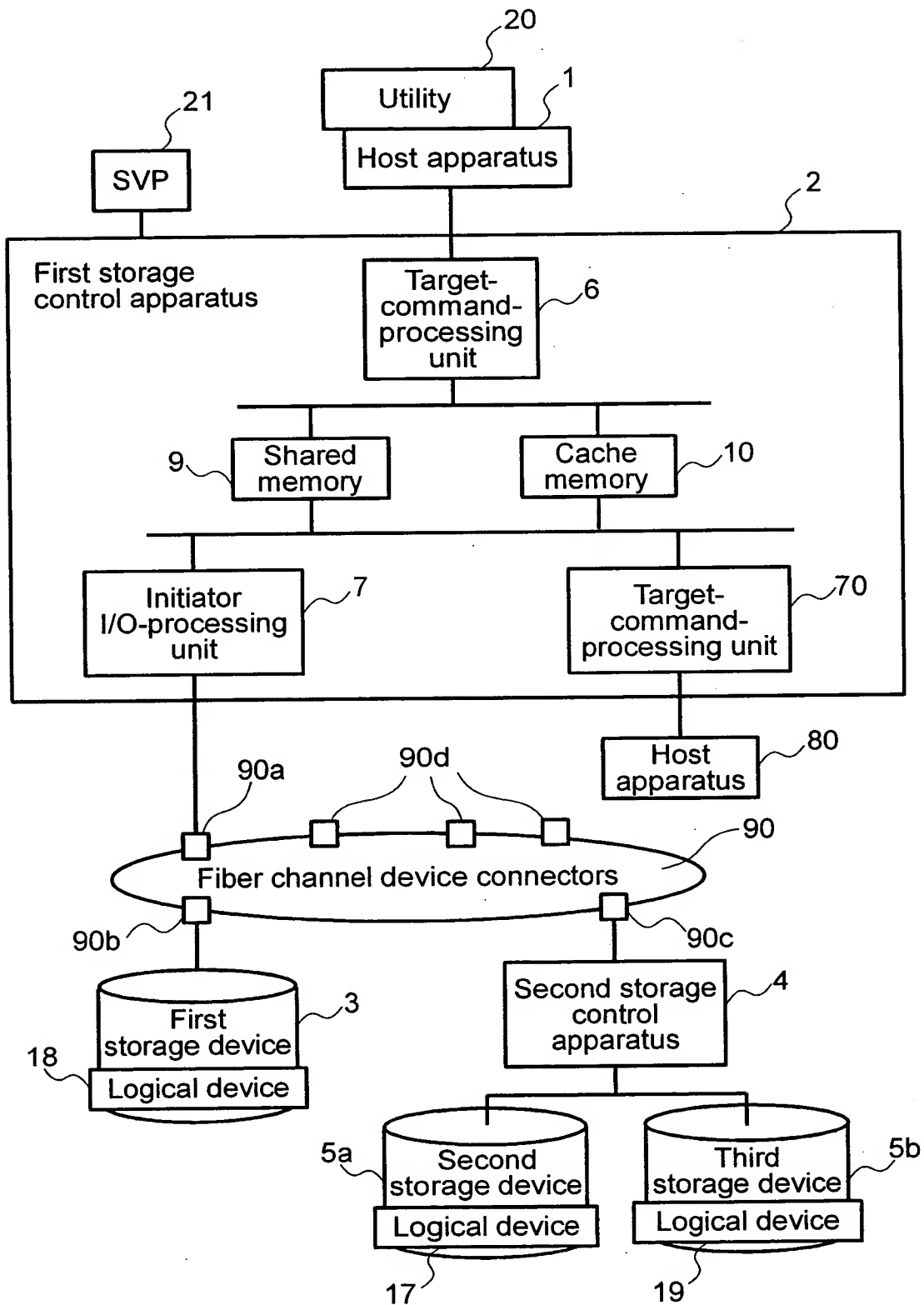


FIG.13



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FIG.14

101 Control- apparatus number	102 Case location	103 Initiator control unit number	104 Node name	105 Address
0	Self	0	HTC	0000E8
1	Other	0	SBC	0000E4
2	Not applicable	Not applicable	Not applicable	Not applicable
.
.
.

FIG.15

